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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki, et al. Art Unit: 2871  
Serial No.: 09/126,826 Examiner: Dung T. Nguyen  
Filed : July 31, 1998  
Title : SEMICONDUCTOR DISPLAY DEVICES (AS AMENDED)

Commissioner for Patents  
Washington, D.C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Further to the Information Disclosure Statement filed March 11, 2003 (PTO-1449 attached), Applicants now submit full English translations of the Japanese patent applications 04-170520 and 04-178633. A new PTO-1449 is filed herewith to list the translations for the examiner's initials after consideration.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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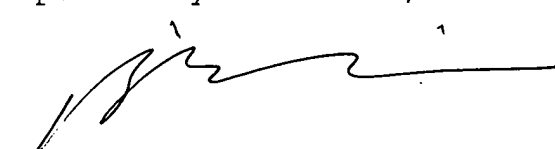
Sharon Gebhart  
Name of Person Signing Certificate

The original Information Disclosure Statement was made before the receipt of a first Office Action on the merits. No fee is required.

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Respectfully submitted,

Date: April 4, 2003

  
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